

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brendan K. Bridgford
Assignee: Xilinx, Inc.
Title: "A Method of Debugging PLD Configuration Using
Boundary Scan"
Serial No.: 10/606,728 File Date: 06-26-2003
Examiner: Amine Riad Art Unit: 2819
Docket No.: X-1216 US Conf. No.: 5830

Mail Stop Amendment
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the two (2) references listed in the attached Substitute for Form PTO-1449A/PT.

The cited references are being cited after the receipt of a first office action but prior to an allowance.

Citation of the above document shall not be construed as an admission that the document is necessarily prior art with respect to the instant invention. Citation of the above document shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above document shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

The Commissioner is hereby authorized to charge the fee of \$180.00 due under 1.17(p) and any additional fees, or credit any overpayment which may be required to Deposit Account No. 24-0040.

Respectfully submitted,

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*I hereby certify that this correspondence is being filed via EFS-Web with
the United States Patent & Trademark Office on April 1, 2008.*

/Katherine Stofer/

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JL:ks